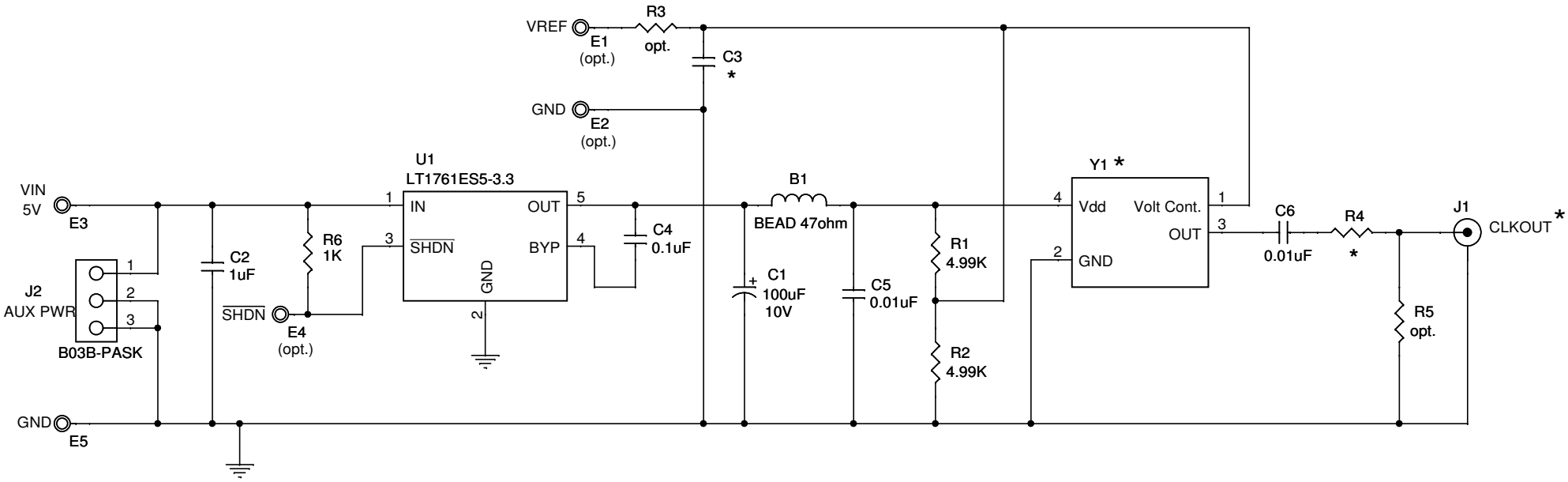
REVISION HISTORY				
ECO	REV	DESCRIPTION	APPROVED	DATE
—	A1	ADD "-D" VERSION	MICHEL A.	10-03-12



* VERSION TABLE

ASSEMBLY TYPE	Y1	CLKOUT Freq. (MHz)	R4	C3
DC1216A-A	Crystek, 601964	100MHz	5 ohms	OPT
DC1216A-B	Crystek, 602017	122.88MHz	5 ohms	OPT
DC1216A-C	Crystek, 602019	80MHz	5 ohms	OPT
DC1216A-D	Crystek, 601964	100MHz, PLL Reference	100 ohms	4.7uF

CONTRACT NO.

APPROVALS	DATE
DRAWN June Wu	1/17/07
ENGINEER Clarence Mayott	1/17/07
CHECKED	
APPROVED	

Wednesday, October 03, 2012

LINEAR TECHNOLOGY
1630 McCarthy Blvd.
Milpitas, CA 95035
Phone: (408)432-1900
Fax: (408)434-0507

TITLE: High Speed ADC Clock Source

SIZE	CAGE CODE	DWG NO	REV
		DC1216A	A1

SCALE: | FILENAME: | SHEET 1 OF 1